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09/912,683

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Austin H. Lesea

Assignee: Xilinx, Inc.

Title: Integrated Testing of Serializer/Deserializer in FPGA

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AMENDMENT IN RESPONSE TO FINAL OFFICE ACTION

In response to the Final Office Action mailed from the Patent Office on August 17, 2004, Applicant submits the following amendments and remarks.

Amendments to the Claims begins on page 2 of this paper.

Remarks begin on page 6 of this paper.